



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : **Mail Stop: ISSUE FEE**  
Koichi HIRANO et al. : **Confirmation No. 5709**  
Serial No. 10/726,675 : **Group Art Unit 2831**  
Filed December 4, 2003 : **Examiner Nguyen T. Ha**  
CAPACITOR AND METHOD FOR : **Attorney Docket No. 2003\_1690A**  
PRODUCING THE SAME, AND CIRCUIT  
BOARD WITH A BUILT-IN CAPACITOR  
AND METHOD FOR PRODUCING THE SAME

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**COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In item 3 of the Notice of Allowability dated July 26, 2006, the Examiner indicated that Applicants' submitted arguments stating that Shimada et al. in view of Tadanobu were silent with respect to certain features in the claims, and that these arguments were found to be persuasive.

However, regarding the Tadanobu reference (US 2003/0039093 and US 6,680,841) mentioned by the Examiner, Applicants note that this reference does not qualify as prior art against the present application. Accordingly, Applicants note that the Examiner's indication that an argument was presented indicating that Tadanobu is silent with respect to certain features of the claims is not correct. Instead, Applicants pointed out that this reference does not qualify as prior art against the present application.

It is kindly requested that these comments be entered into the above-identified application file. No response from the Examiner is deemed to be necessary.

Respectfully submitted,

Koichi HIRANO et al.

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